

IN THE SPECIFICATION:

Please amend the 13 contiguous paragraphs starting at page 1, line 5 and ending at page 2, line 6 as shown below.

05/22/04 (DW)
--U.S. Patent Application Ser. No. 09/652,644^{, now patent No. 6701387,} (15311-2281) entitled ADAPTIVE DATA PREFETCH PREDICTION ALGORITHM;

U.S. Patent Application Ser. No. 09/653,133^{, now patent No. 6456510,} (15311-2282) entitled UNIQUE METHOD OF REDUCING LOSSES IN CIRCUITS USING V² PWM CONTROL;

U.S. Patent Application Ser. No. 09/652,641 (15311-2283) entitled IO SPEED AND LENGTH PROGRAMMABLE WITH BUS POPULATION;

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U.S. Provisional Patent Application Ser. No. 60/304,167 (15311-2285) entitled SYSTEM AND METHOD FOR USING FUNCTION NUMBERS TO INCREASE THE COUNT OF OUTSTANDING SPLIT TRANSACTIONS;

U.S. Patent Application Ser. No. 09/652,984^{, now patent No. 6647453,} (15311-2286) entitled SYSTEM AND METHOD FOR PROVIDING FORWARD PROGRESS AND AVOIDING STARVATION AND LIVELOCK IN A MULTIPROCESSOR COMPUTER SYSTEM;

U.S. Patent Application Ser. No. 09/653,180^{, now patent No. 6681282,} (15311-2287) entitled ONLINE ADD/REMOVAL OF SERVER MANAGEMENT INFRASTRUCTURE;

U.S. Patent Application Ser. No. 09/652,494^{, now patent No. 6640272} (15311-2288) entitled AUTOMATED BACKPLANE CABLE CONNECTION IDENTIFICATION SYSTEM AND METHOD;

U.S. Patent Application Ser. No. 09/652,459 (15311-2289) entitled CLOCK FORWARDING DATA RECOVERY AUTOMATED BACKPLANE CABLE CONNECTION IDENTIFICATION SYSTEM AND METHOD;

, now patent No 6629257,
U.S. Patent Application Ser. No. 09/652,980 ^V(~~15311-2290~~) entitled CLOCK
FORWARD INITIALIZATION AND RESET SIGNALING TECHNIQUE;

U.S. Patent Application Ser. No. 09/944,515 (~~15311-2292~~) entitled PASSIVE
RELEASE AVOIDANCE TECHNIQUE;

, now patent No 6633367,
U.S. Patent Application Ser. No. 09/652,985 ^V(~~15311-2293~~) entitled COHERENT
TRANSLATION LOOK-ASIDE BUFFER;

, now patent No 6724850,
U.S. Patent Application Ser. No. 09/652,645 ^V(~~15311-2294~~) entitled DETERMI-
NISTIC HARDWARE BEHAVIOR BETWEEN MULTIPLE ASYNCHRONOUS
CLOCK DOMAINS THROUGH THE NOVEL USE OF A PLL; and

, now patent No 6711693,
U. S. Patent Application Ser. No. 09/655,171 ^V(~~15311-2306~~) entitled VIRTUAL
TIME OF YEAR CLOCK.--

Kindly delete the paragraph starting at page 40, line 25 and continuing to page 41,
line 4 as shown below.

Trademarks

~~VxWorks is a trademark of Wind River~~

~~AMD, is a trademark of Advanced Micro Devices.~~

~~Am186, is a registered trademark of Advanced Micro Devices.~~

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~~LM80, is a trademark of National Semiconductor Corporation.~~

~~I²C, is a trademark of Philips Semiconductors.~~